

Amendments to the Specification

Please delete the paragraph beginning at page 8, line 26 and ending at page 9, line 10 and replace it with the following paragraph:

Phase-frequency detector 201 compares an input signal, such as a RefClk signal, to a FdbkClk signal and generates a phase difference to charge pump 202. For example, up and down signals are generated by phase-frequency detector 201. Charge pump 202 includes a gain or corresponding current that is adjustable, or in other words increased or decreased, responsive to a FreqSel signal. A V_{pump} voltage is then provided to filter 203. In an embodiment of the present invention, filter 203 is a low pass filter including an adjustable ~~resister~~ resistor that is increased or decreased responsive to a FreqSel signal. A voltage is then provided to a voltage regulator and in particular Amp 204 in an embodiment of the present invention. Amp 204 acts as a buffer and generates a buffered voltage V_c to VCOs 205 and 206. In an embodiment of the present invention, VCO 205 outputs an oscillating signal having a first frequency range responsive to voltage V_c and VCO 206 outputs an oscillating signal having a second frequency range responsive to voltage V_c .